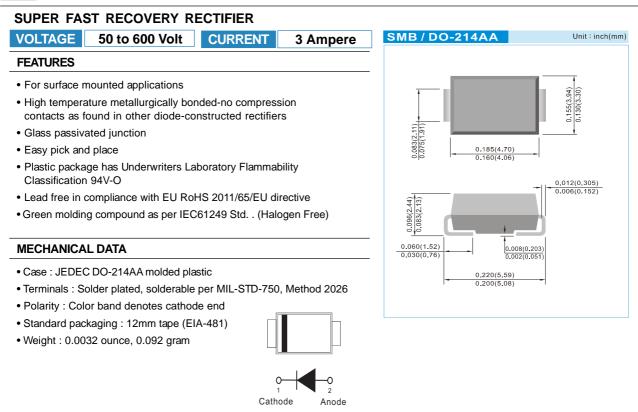
December 31,2015-REV.01

SEMI CONDUCTOR	

PAN





MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate current by 20%.

PARAMETER	SYMBOL	ER3AA	ER3BA	ER3CA	ER3DA	ER3EA	ER3GA	ER3JA	UNITS
Maximum Recurrent Peak Reverse Voltage	V _{RRM}	50	100	150	200	300	400	600	v
Maximum RMS Voltage	V _{rms}	35	70	105	140	210	280	420	v
Maximum DC Blocking Voltage	V _{DC}	50	100	150	200	300	400	600	v
Maximum Average Forward Current	I _{F(AV)}	3							A
Peak Forward Surge Current : 8.3ms single half sine-wave superimposed on rated load	I _{fsm}	80							А
Maximum Forward Voltage at 3A	V _F	0.95 1.25 1.7					1.7	V	
Maximum DC Reverse Current at Rated DC Blocking Voltage	I _R	1							μA
Maximum Reverse Recovery Time (Notes 3)	t _{rr}	35							ns
Typical Junction Capacitance Measured at 1MHz and applied V _R =4V	C	45							рF
Typical Thermal Resistance (Notes 2) (Notes 1)		135 20							°C / W
Operating and Storage Temperature Range	T _J ,T _{stg}	-55 to +150						°C	

NOTES:1. Mounted on a FR4 PCB, single-sided copper, with $100 \mbox{cm}^2$ copper pad area

2. Mounted on a FR4 PCB, single-sided copper, mini pad.

3. Reverse Recovery Test Conditions: $I_{\text{F}}{=}0.5\text{A}$, $I_{\text{R}}{=}{-}1\text{A}$ Irr=-0.25A



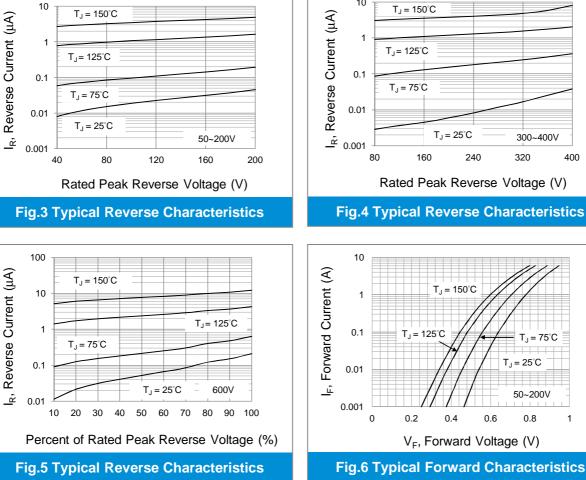
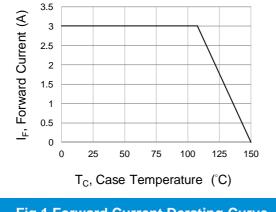
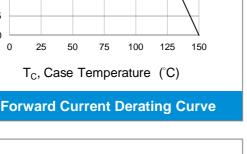


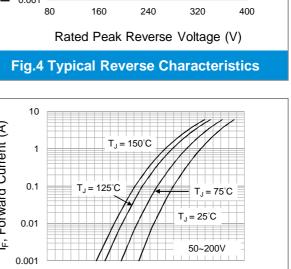
Fig.1 Forward Current Derating Curve







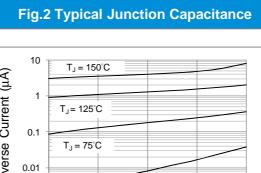
ER3AA~ER3JA



10

V_R, Reverse Bias Voltage (V)

100





100

10

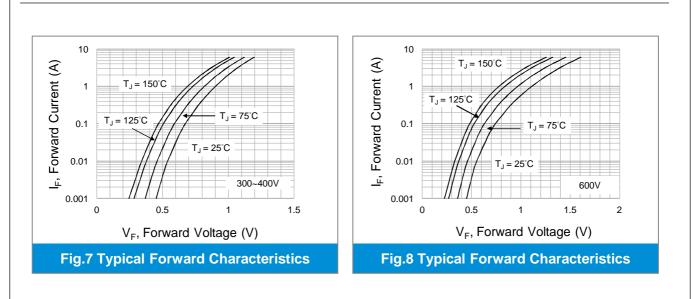
1

C_J, Junction Capacitance (pF)





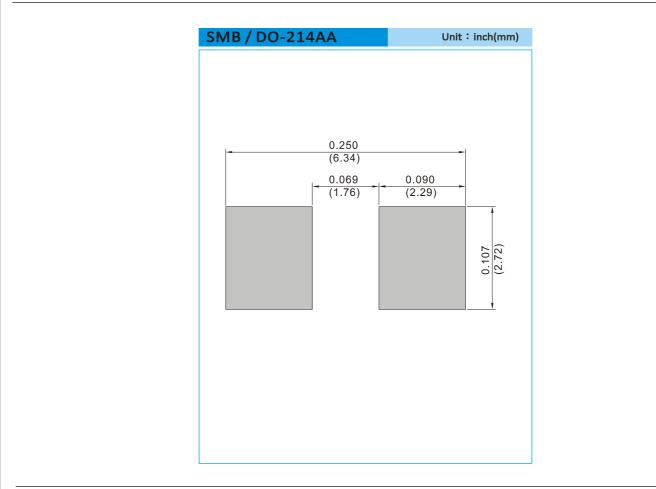
ER3AA~ER3JA





ER3AA~ER3JA

MOUNTING PAD LAYOUT



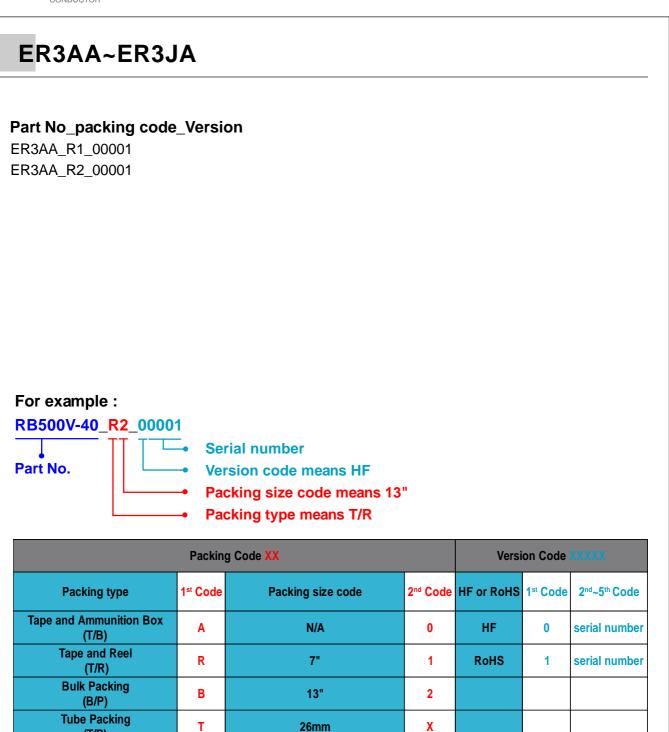
ORDER INFORMATION

Packing information

T/R - 3K per 13" plastic Reel

T/R - 0.5K per 7" plastic Reel





26mm

52mm

PANASERT T/B CATHODE UP

(PBCU) PANASERT T/B CATHODE DOWN

(PBCD)

Χ

Y

U

D

(T/P) Tape and Reel (Right Oriented)

(TRR) Tape and Reel (Left Oriented)

(TRL)

FORMING

S

L

F





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